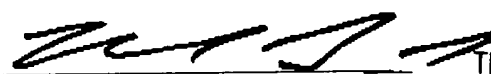


PATENT

FO466

CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this correspondence (along with any paper referred to as being attached or enclosed) is being sent via facsimile to: 703-872-9318, Box AF, Assistant Commissioner for Patents, U.S. Patent and Trademark Office, Washington, D.C. 20231

Date: 12-10-02
Himanshu S. Amin

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TECHNOLOGY CENTER 2800

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:

Applicants: Michael Fliesler, et al.

Examiner: Thomas L. Dickey

Serial No: 10/050,394

Art Unit: 2826

Filing Date: January 16, 2002

Title: IMPROVED DIODE FABRICATION FOR ESD/EOS PROTECTION

Assistant Commissioner for Patents
U.S. Patent and Trademark Office
Washington, D.C. 20231

REPLY TO RESTRICTION REQUIREMENT MAILED NOVEMBER 18, 2002

This Reply is in response to the Restriction Requirement mailed on November 18, 2002 in connection with the above-identified patent application.

The Examiner requires restriction to one of the following three groups of claims:

(I) claims 21-24, (II) 1-8 or (III) 9-20.

Applicants' representative hereby elects with traverse group II (claims 1-8 drawn to a device) for further prosecution on the merits.

10/050,394

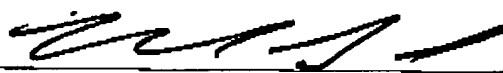
F0466 (AMDP574US)

Should there be any questions regarding this paper, the Examiner is invited to contact applicants' undersigned representative at the telephone number listed below.

In the event any fees are due in connection with submission of this document, the Commissioner is authorized to charge such fees to Deposit Account No. 50-1063.

Respectfully submitted,

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